

ABSTRACT OF THE DISCLOSURE

A solid-state image pickup apparatus includes a mode setting circuit for allowing the operator to select desired one of the modes matching with the display format of a display which displays a video signal fed thereto. A clock generating circuit generates clocks including a first clock and a second clock higher in frequency than the first clock. A frequency selector selects either one of the first and second clocks in accordance with a mode fed from the mode setting circuit. An image pickup section includes a plurality of photosensitive cells for transforming, by photoelectric transduction, light incident thereto from a scene to be picked up. The image pickup section picks up the scene and produces signal charges representative of the scene in accordance with the output of the frequency selector. A noise reducing circuit reduces noise components included in a signal output from the image pickup section. A digitizing circuit converts the output of the noise reducing circuit to a digital signal in accordance with the first clock. A signal processing circuit processes the output of the digitizing circuit in a manner matching with picture display and/or recording. A controller controls the clock generating circuit, frequency selector, image pickup section, noise reducing circuit, digitizing circuit, and signal processing circuit. The modes include a first mode in which the frequency selector outputs the first clock and a second mode in which it outputs the second clock.